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Payton

Applicant: Eichelberger et al.

Serial No: 09/502,078

Examiner: Pizarro-Crespo, M.


Filed: 02/10/00

Group Art Unit: 2814

Title: INTEGRATED CIRCUIT STRUCTURES AND METHODS
EMPLOYING A LOW MODULUS HIGH ELONGATION
PHOTODIELECTRIC

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail - Label No. EV046785282US - in an envelope addressed to: Commissioner for Patents, P.O. Box 2327, Arlington, VA 22202, on December 18, 2001.


Darlene E. Novak

Date of Signature: December 18, 2001

Commissioner for Patents
P.O. Box 2327
Arlington, VA 22202

Response to Office Action

Dear Sir:

This paper is submitted in reply to the Office Action mailed June 20, 2001 in connection with the above-designated application. Since a response to the Office Action was initially due on or before September 20, 2001, a Petition requesting a three (3) month extension of time is filed herewith. Reconsideration and allowance of all claims are

1109.005

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